INTERNATIONAL SEARCH REPORT

International Application No
PCT/US2004/030580

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H03K19/173

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)
EPO-Internal, WPI Data, INSPEC, COMPENDEX, IBM-TDB

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No
X	TIRI K ET AL: "A Dynamic and Differential CMOS Logic with Signal Independent Power Consumption to Withstand Differential Power Analysis on Smart Cards" ESSCIRC 2002, PROCEEDINGS OF THE 28TH EUROPEAN SOLID-STATE CIRCUIT CONFERENCE, 24-26 SEPT. 2002, FLORENCE, ITALY, 2002, pages 403-406, XP002318805 ISBN: 88-900847-9-0 the whole document	1,2, 5-11, 14-20, 24,25

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[X] Further documents are listed in the continuation of box C

[X] Patent family members are listed in annex

° Special categories of cited documents

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance, the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
23 February 2005	07/03/2005

Name and mailing address of the ISA	Authorized officer
European Patent Office, P B 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx 31 651 epo nl, Fax (+31-70) 340-3016	Moll, P

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No
X	HONG-YI HUANG ET AL: "New CMOS differential logic circuits for true-single-phase pipelined systems" CIRCUITS AND SYSTEMS, 1994. ISCAS '94., 1994 IEEE INTERNATIONAL SYMPOSIUM ON LONDON, UK 30 MAY-2 JUNE 1994, NEW YORK, NY, USA,IEEE, US, vol. 4, 30 May 1994 (1994-05-30), pages 15-18, XP010143200 ISBN: 0-7803-1915-X figures 1,3,4	1,2,5,7, 8,10,11, 14,16, 17,24,25
A		19-21

X	US 5 909 127 A (PEARSON ET AL) 1 June 1999 (1999-06-01) column 6, lines 14-24; figure 3 column 6, line 64 - column 8, line 13; figures 7-10	1,2,7,8, 19,20
A		10,21, 24,25

X	EP 0 147 598 A (INTERNATIONAL BUSINESS MACHINES CORPORATION) 10 July 1985 (1985-07-10) page 6, line 34 - page 8, line 12; figure 3	1,2,5, 7-9
A		10, 19-21, 24,25

INTERNATIONAL SEARCH REPORT

Information on patent family members

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PCT/US2004/030580

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5909127 A	01-06-1999	IE 960781 A1	02-07-1997
EP 0147598 A	10-07-1985	US 4570084 A	11-02-1986
		DE 3471413 D1	23-06-1988
		EP 0147598 A1	10-07-1985
		JP 60114029 A	20-06-1985